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Part Number: KTIR0911S

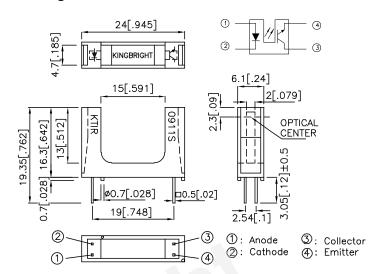
Package Dimensions

Features

- •Ultra-Small.
- •Minimal influencefrom stray light.
- •Low collector-emitter saturation Voltage.
- •RoHS compliant.

Applications

- •Optical control equipment.
- Cameras.
- •Floppy disk drives.



Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.25(0.01") unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
- 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

Absolute Maximum Ratings(Ta=25°C)

	Parameter	Symbol	Rating	Unit
Input	Forward current	IF	50	mA
	Reverse voltage	VR	6	V
	Power dissipation	Pd	75	mW
	Peak Forward Current (Pulse Width <100uS,Duty Cycle=1%)	I _{FP}	1	А
Output	Collector-emitter voltage	VCEO	35	V
	Emitter-collector voltage	VECO	6	V
	Collector current	IC	20	mA
	Collector power dissipation	PC	75	mW
Operating temperature		Topr	-25~+85	°C
Storage	Storage temperature		-40~+100	°C
Soldering	temperature (1/16 inch from body for 5 seconds)	econds) Tsol 260		°C

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 DRAWN: Q.M.Chen
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Electro-optical Characteristics(Ta=25°C)

Parameter		Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input	Forward voltage		VF	IF=20mA	1.0	1.2	1.5	V
	Reverse current		IR	VR=6V	-	-	10	uA
	Peak Wavelength		λΡ	IF=20mA	-	940	-	nm
Output	Collector dark current		ICEO	VCE =20V	_	_	100	nA
	Collector-emitter saturation voltage		VCE(SAT)	IC=1mA IF=40mA	-	_	0.4	V
Transfer Charac-	Current transfer ratio		CTR	VCE=5V IF=20mA	_	9.5	_	%
teristics	Response time	Rise time	tr	VCE=2V IC=2mA RL=100Ω	-	5	25	μSec
		Fall time	tf		_	4	20	μSec

Fig.1 FORWARD CURRENT Vs. FORWARD VOLTAGE

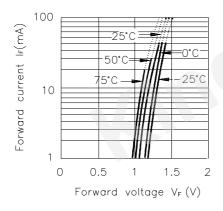


Fig.2 COLLECTOR CURRENT Vs. FORWARD CURRENT

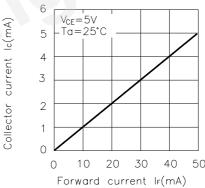
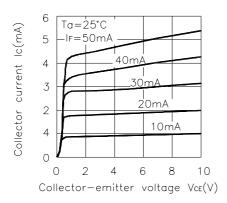


Fig.3 COLLECTOR CURRENT VS.

COLLECTOR-EMITTER VOLTAGE



Relative sensitivity(%) 1100 1200 Wavelength ⋋ (nm) Spectral Sensitivity

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Fig.4 COLLECTOR CURRENT Vs.

AMBIENT TEMPERATURE

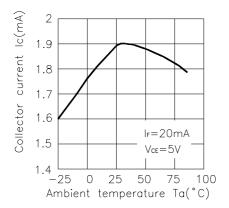


Fig.5 COLLECTOR—EMITTER SATURATION VOLTAGE Vs. AMBIENT TEMPERATURE

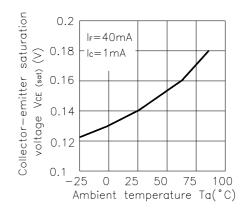


Fig.6 RELATIVE COLLECTOR CURRENT Vs. SHIELD DISTANCE (1)

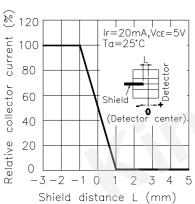


Fig.7 RELATIVE COLLECTOR CURRENT Vs. SHIELD DISTANCE (2)

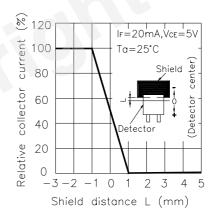
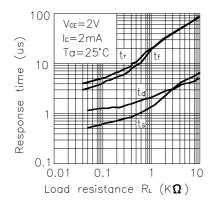
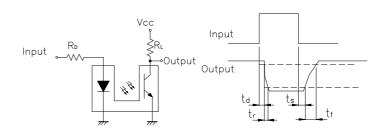


Fig.8 RESPONSE TIME Vs. LOAD RESISTANCE



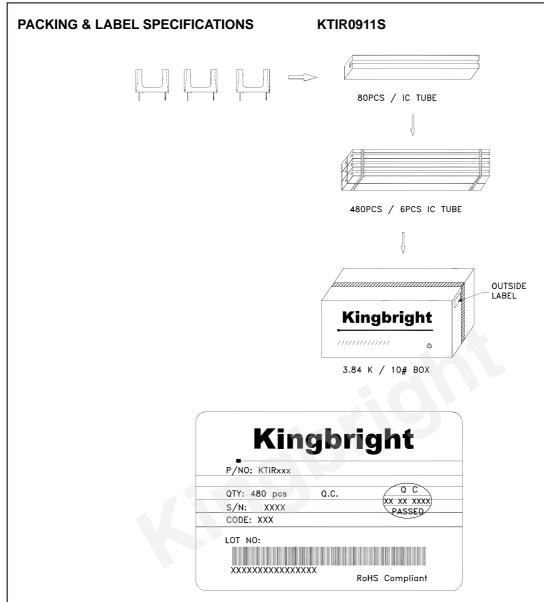
Test Circuit for Response Time



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