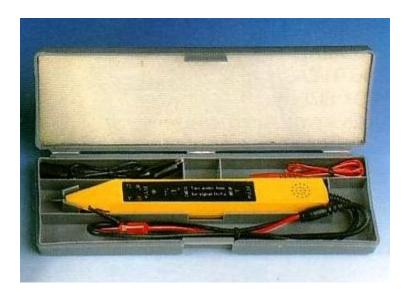
Brought to you by



Web: www.communica.co.za || Email: info@communica.co.za

Logic Test Probe

LOGIC PROBE 3



COMMON SPECIFICATIONS:

- input impedance: 1MΩ.
- width of min, pulse: 30 ns.
- max.input frequency: 17 MHz.
- measurable Logic Gate: TTL and CMOS Logic 1 (red LED Lights) and then buzz.
 2.3V ± 0.2V 70% Vcc

Logic 0 (green LED lights) and then buzz. 0.8V \pm 0.2V 30% Vcc.

- input overvoltage protection: ±220V DC/AC 15 sec.
- Pulse (yellow LED) flashing time: 500ms.
- Power supply protection: ±20V.

dimension	weight
212×26.5×18mm (main body)	65g
235×78×32mm (with case)	210g

Special functions for LP-3500

- ABS CASE
- Two audio tones
- Two external terminal.