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DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N1893 is a silicon NPN epitaxial planar transistor designed for small signal general purpose switching applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

TO-39 CASE

	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	120	V
Collector-Emitter Voltage	VCER	100	V
Collector-Emitter Voltage	V _{CEO}	80	V
Emitter-Base Voltage	V _{EBO}	7.0	V
Continuous Collector Current	ΙC	500	mA
Power Dissipation (T _C =25°C)	PD	3.0	W
Power Dissipation	PD	0.8	W
Operating and Storage Junction Temperature	TJ, Tstg	-65 to +200	°C

ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)				
SYMBOL	TEST CONDITIONS V _{CB} =90V	MIN	MAX 10	UNITS nA
СВО	00			
IEBO	V _{EB} =5.0V		10	nA
BVCBO	Ι _C =100μΑ	120		V
BVCER	I _C =10mA, R _{BE} =10Ω	100		V
BVCEO	I _C =10mA	80		V
BVEBO	I _E =100μΑ	7.0		V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		1.2	V
V _{CE(SAT)}	I _C =150mA, I _B =15mA		5.0	V
V _{BE(SAT)}	I _C =50mA, I _B =5.0mA		0.9	V
V _{BE(SAT)}	I _C =150mA, I _B =15mA		1.3	V
h _{FE}	V _{CE} =10V, I _C =100µA	20		
h _{FE}	V _{CE} =10V, I _C =10mA	35		
h _{FE}	V _{CE} =10V, I _C =150mA	40	120	
f _T	V _{CE} =10V, I _C =50mA, f=20MHz	50		MHz
Cob	V _{CB} =10V, I _E =0, f=100kHz		15	pF
C _{ib}	V _{EB} =0.5V, I _C =0, f=100kHz		85	pF

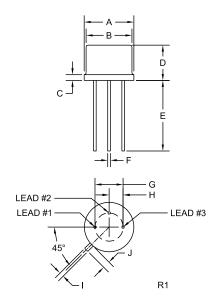
R1 (23-April 2013)



2N1893

SILICON NPN TRANSISTOR

TO-39 CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.335	0.370	8.51	9.40		
B (DIA)	0.315	0.335	8.00	8.51		
С	-	0.040	-	1.02		
D	0.240	0.260	6.10	6.60		
E	0.500	-	12.70	-		
F (DIA)	0.016	0.021	0.41	0.53		
G (DIA)	0.200		5.08			
Н	0.100		2.54			
	0.028	0.034	0.71	0.86		
J	0.029	0.045	0.74	1.14		
TO-39 (REV R1)						

TO-39 (REV: R1)

LEAD CODE:

1) Emitter

2) Base

3) Collector

MARKING: FULL PART NUMBER

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

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