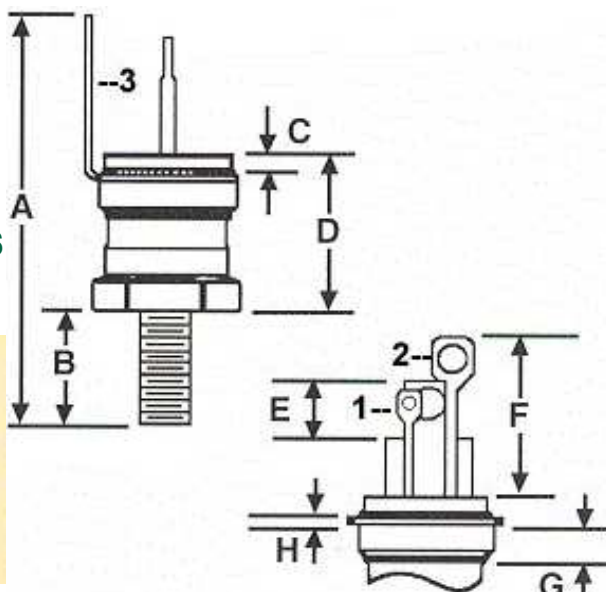


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**30 and 40 AMP 1/2" PRESS FIT ISOLATED STUD MOUNT TRIAC**



- 1. GATE
- 2. MT-1
- 3. MT-2 (collar)

SYM.	INCHES	
A	1.400	
B	0.453	0.422
C	0.090	
D	0.670	0.625
E	0.275	
F	0.670	0.610
G	0.100	
H	0.025	

-Warning-  
 Isolated stud products should be handled with care. The ceramic used in these thyristers contains BERYLLIUM OXIDE as a major ingredient. DO NOT crush, grind, or abrade these portions if the thyristers because the dust resulting from such action may be HAZARDOUS if INHALED.

MAXIMUM RATINGS	SYMBOL	DEVICE NUMBERS		UNITS
REPETITIVE PEAK OFF-STATE VOLTAGE (1)GATE OPEN, AND $T_J = 110^\circ\text{C} / \text{VDRM}$	200 400 600	SIPT230 SIPT430 SIPT630	SIPT240 SIPT440 SIPT640	VOLT
RMS ON-STATE CURRENT AT $T_C = 80^\circ\text{C}$ AND CONDUCTION, ANGLE OF $360^\circ$	IT(RMS)	30	40	AMP
PEAK SURGE (NON-REPETITIVE) ON-STATE CURRENT, ONE-CYCLE, AT 50HZ OR 60HZ	ITSM	300	400	AMP
PEAK GATE - TRIGGER CURRENT FOR $3\mu\text{SEC. MAX.}$	IGTM	12	12	AMP
PEAK GATE - POWER DISSIPATION AT $IGT \leq IGTM$	PGM	40	40	WATT
AVERAGE GATE - POWER DISSIPATION	PG(AV)	0.75	0.75	WATT
STORAGE TEMPERATURE RANGE	TSTG	-40 to +150		$^\circ\text{C}$
OPERATING TEMPERATURE RANGE, $T_J$	TOPER	-40 to +110		$^\circ\text{C}$
PEAK OFF - STATE CURRENT (1) GATE OPEN $T_C = 110^\circ\text{C}$ $V_{DRM} = \text{MAX.}$ RATING	IDRM	1.0	1.0	MA MAX.
MAXIMUM ON - STATE VOLTAGE, (1) AT $T_C = 25^\circ\text{C}$ AND $I_T = \text{RATED AMPS}$	VTM	2.0	2.0	VOLT MAX.
DC HOLDING CURRENT, (1) GATE OPEN AND $T_C = 25^\circ\text{C}$	IHO	60	60	MA MAX.
CRITICAL RATE-OF-RISE OF OFF-STATE VOLTAGE, (1) FOR $V_D = V_{DRM}$ GATE OPEN, $T_C = 110^\circ\text{C}$	CRITICAL $dv/dt$	200	200	$\text{V}/\mu\text{SEC.}$
CRITICAL RATE-OF-RISE OF COMMUTATION VOLTAGE,(1) AT $T_C = 80^\circ\text{C}$ , GATE UNENERGIZED, $V_D = V_{DRM}$ , $I_T = I_T(\text{RMS})$	COMMUTATING $dv/dt$	3	3	$\text{V}/\mu\text{SEC.}$
DC GATE - TRIGGER CURRENT FOR $V_D = 12\text{VDC}$ . $R_L = 30\text{ ohm}$ AND AT $T_C = 25^\circ\text{C}$ ( $T_2 + \text{GATE} + T_2 - \text{GATE-}$ ) Q1 & 3 ( $T_2 + \text{GATE} - T_2 - \text{GATE} +$ ) Q 2 & 4	IGT*	100 I, III 150 II, IV	100 I, III 150 II, IV	MA MAX.
DC GATE - TRIGGER VOLTAGE FOR $V_D = 12\text{VDC}$ . $R_L = 30\text{ ohm}$ AND AT $T_C = 25^\circ\text{C}$	VGT	2.5	2.5	VOLT MAX.
GATE CONTROLLED TURN-ON TIME FOR $V_D = V_{DRM}$ $IGT = 200\text{MA}$ , $T_R = 0.1\mu\text{SEC}$ . $I_T = 10\text{A}$ (PEAK) AND $T_C = 25^\circ\text{C}$	TGT	3	3	$\mu\text{SEC.}$
THERMAL RESISTANCE, JUNCTION-TO-CASE	$R_{\theta J-C}$	2.1	2.1	$^\circ\text{C} / \text{WATT TYP}$

Notes:(1) All values apply in either direction. \*Other gate options available; Consult factory