### T-1 3/4 (5mm) BI-COLOR INDICATOR LAMP

Part Number: L-59SURKCGKC

Hyper Red Green

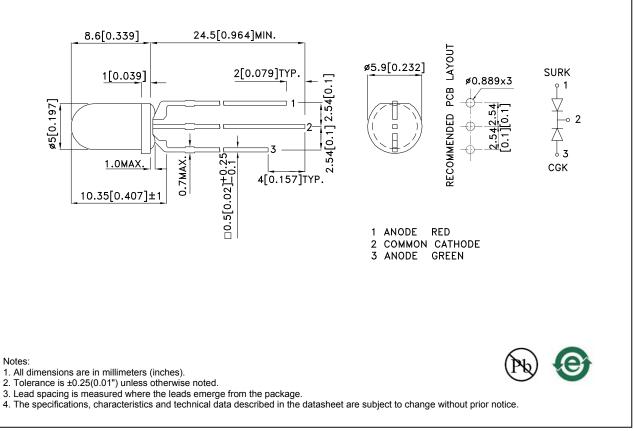
#### Features

- Uniform light output.
- Low power consumption.
- 3 leads with one common lead.
- Long life-solid state reliability.
- RoHS compliant.

#### Descriptions

- The Hyper Red source color devices are made with Al GaInP on GaAs substrate Light Emitting Diode.
- The Green source color devices are made with AlGaInP on GaAs substrate Light Emitting Diode.

#### **Package Dimensions**



SPEC NO: DSAB8511 APPROVED: WYNEC REV NO: V.17A CHECKED: Allen Liu DATE: FEB/06/2015 DRAWN: P.Cheng PAGE: 1 OF 7 ERP: 1101005940

#### **Selection Guide** Viewing lv (mcd) [2] @ 20mA Angle [1] Part No. Dice Lens Type Min. 201/2 Тур. 1800 2800 Hyper Red (AlGaInP) \*600 \*1200 L-59SURKCGKC Water Clear 24° 450 900 Green (AlGaInP) \*450 \*900

Notes:

1.  $\theta$ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

Luminous intensity/ luminous Flux: +/-15%.
\*Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

#### Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Hyper Red Green	645 574		nm	I⊧=20mA
λD [1]	Dominant Wavelength	Hyper Red Green	630 570		nm	I⊧=20mA
Δλ1/2	Spectral Line Half-width	Hyper Red Green	28 20		nm	I⊧=20mA
С	Capacitance	Hyper Red Green	35 15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Hyper Red Green	1.95 2.1	2.5 2.5	V	I⊧=20mA
lr	Reverse Current	Hyper Red Green		10 10	uA	VR = 5V

Notes:

1.Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.

3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

4. Excess driving current and/or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

#### Absolute Maximum Ratings at TA=25°C

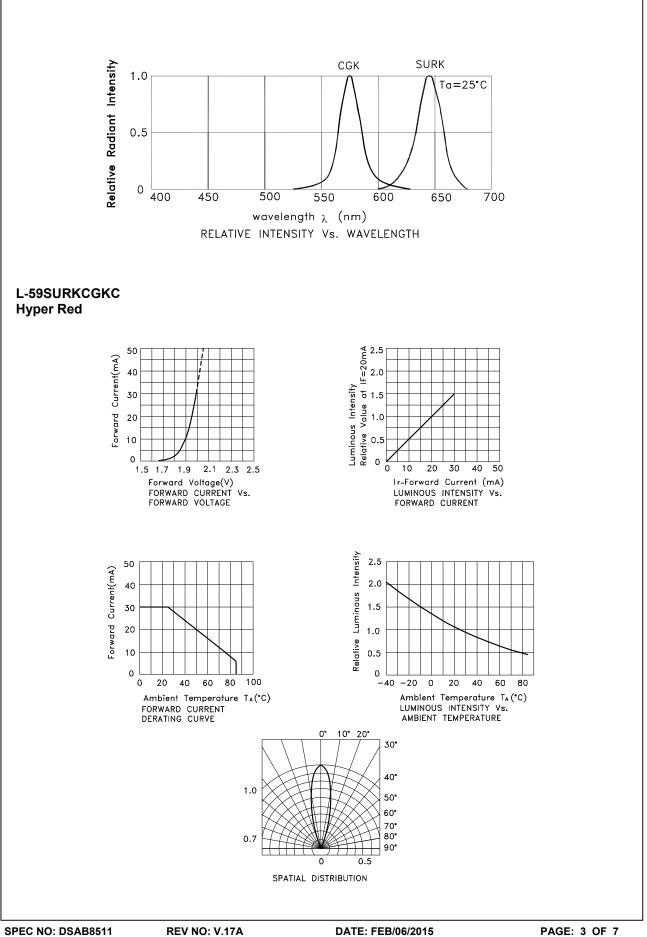
Parameter	Hyper Red	Green	Units		
Power dissipation	75	75	mW		
DC Forward Current	30	30	mA		
Peak Forward Current [1]	[1] 185 150		mA		
Reverse Voltage	Į	V			
Operating / Storage Temperature	-40°C To +85°C				
Lead Solder Temperature [2]	Solder Temperature [2] 260°C For 3 Seconds				
Lead Solder Temperature [3]	60°C For 5 Seconds				

Notes:

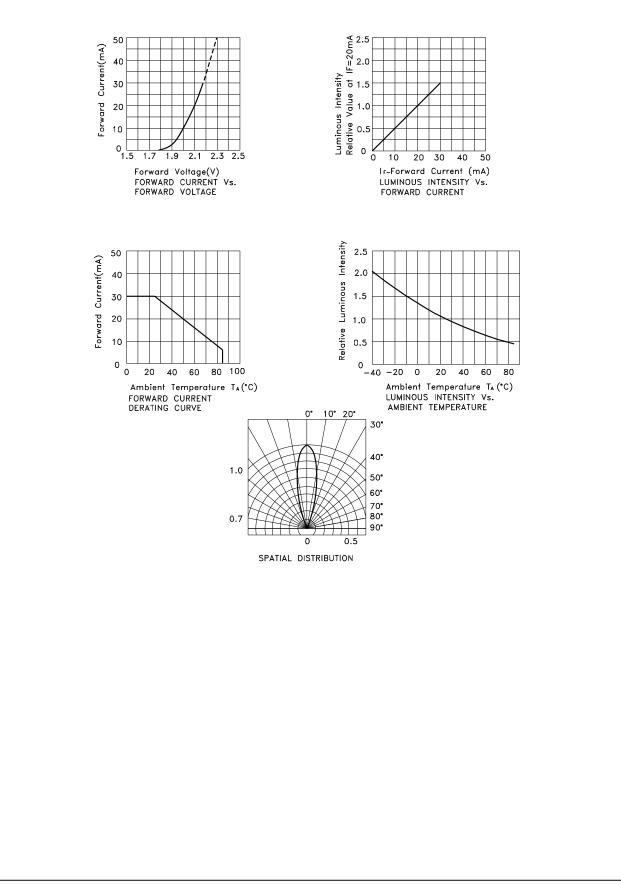
1. 1/10 Duty Cycle, 0.1ms Pulse Width.

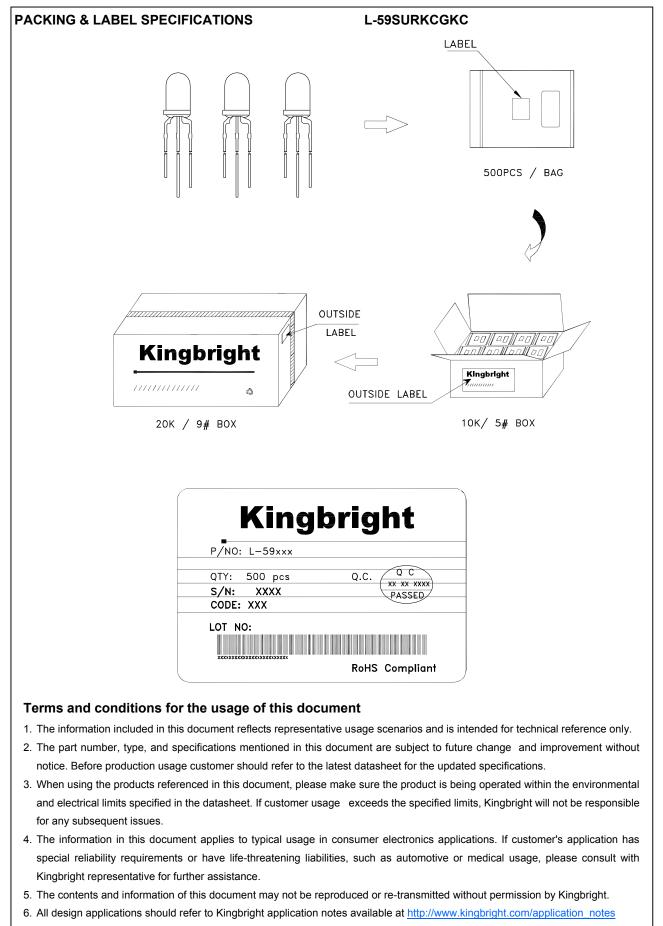
2. 2mm below package base.

3. 5mm below package base.



Green

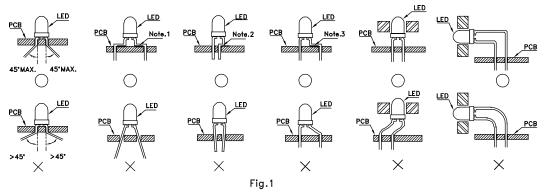




DATE: FEB/06/2015 DRAWN: P.Cheng

#### PRECAUTIONS

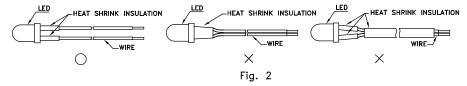
- 1. Storage conditions:
  - a.Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
  - b.LEDs should be stored with temperature  $\leq$  30°C and relative humidity < 60%.
  - c.Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.
- 2. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



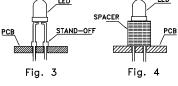
" $\bigcirc$ " Correct mounting method "imes" Incorrect mounting method

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

3. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure. (Fig. 2)



4. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



- 5. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
- 6. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

