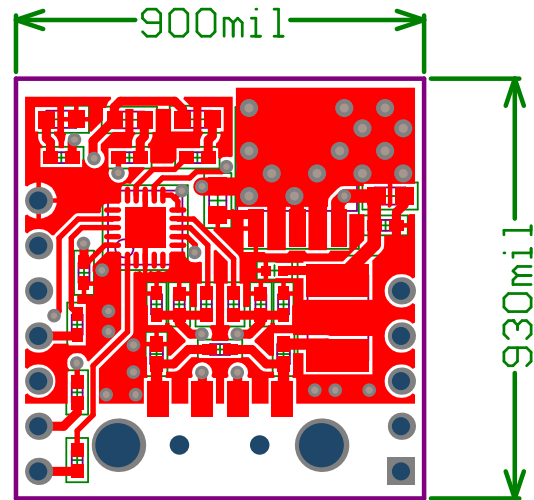


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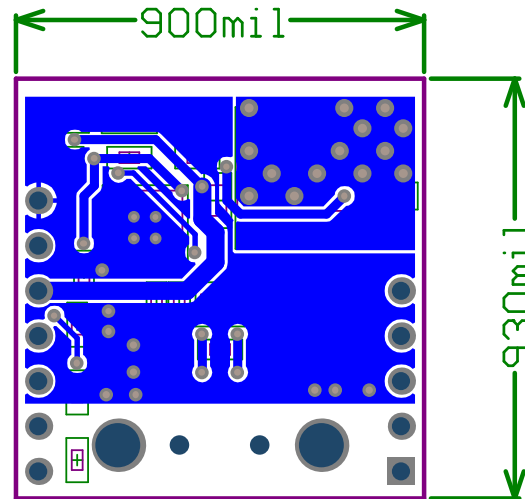


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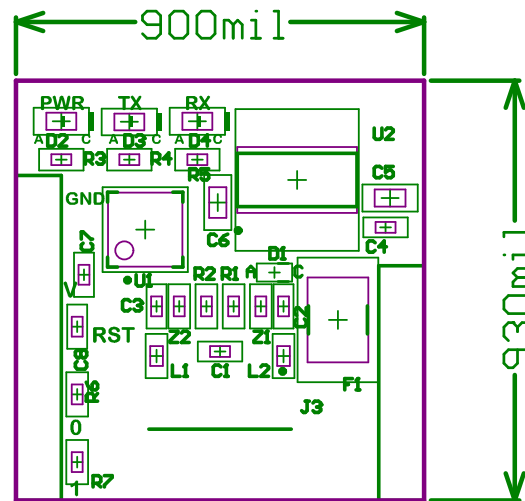
 BOTTOM COPPER

 DIMENSIONS

 ROUTING KEEPOUT LAYER

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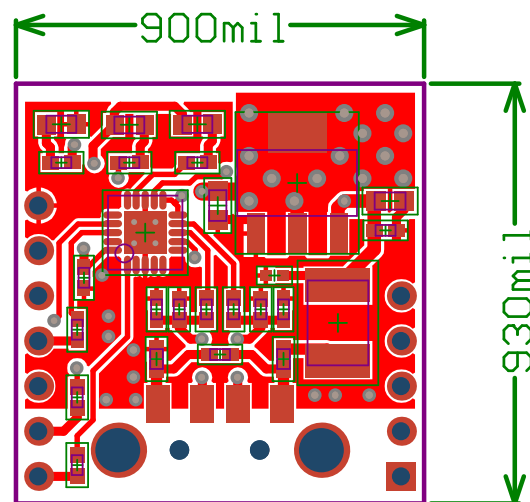
TOP OVERLAY

DIMENSIONS

ROUTING KEEPOUT LAYER

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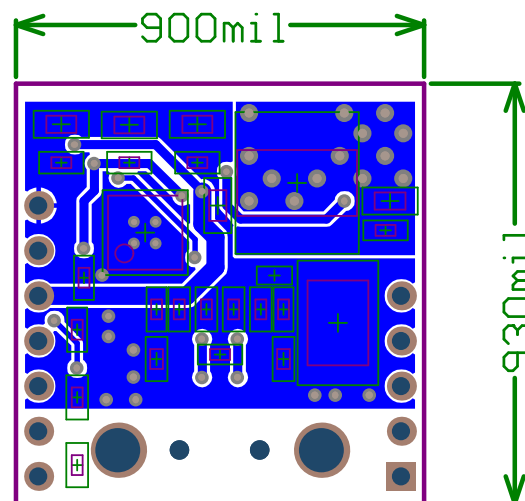


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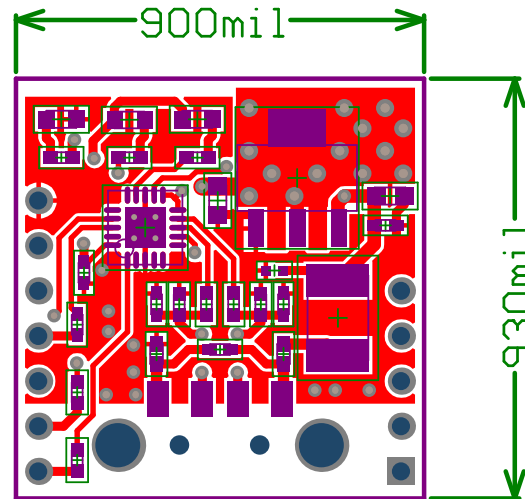


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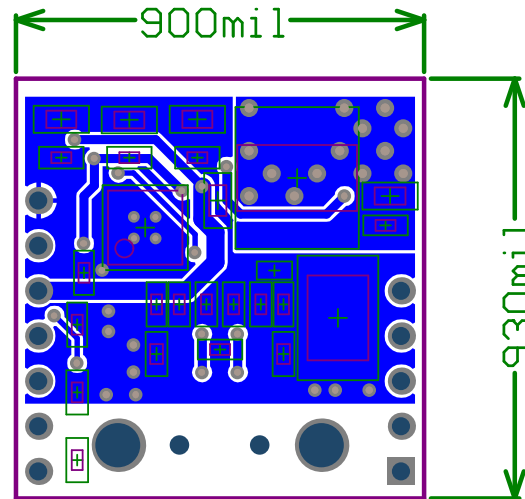


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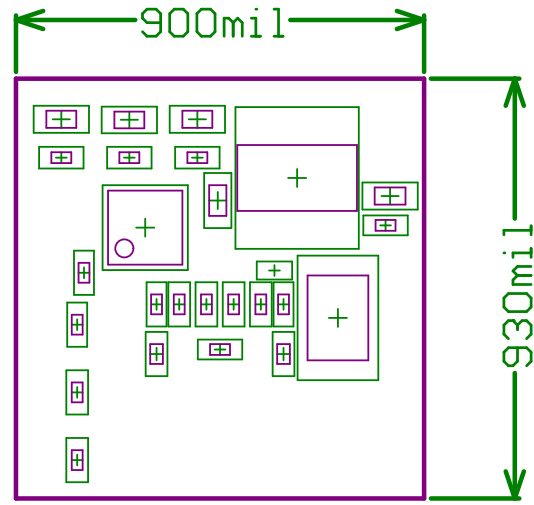
 BOTTOM PASTE

 DIMENSIONS

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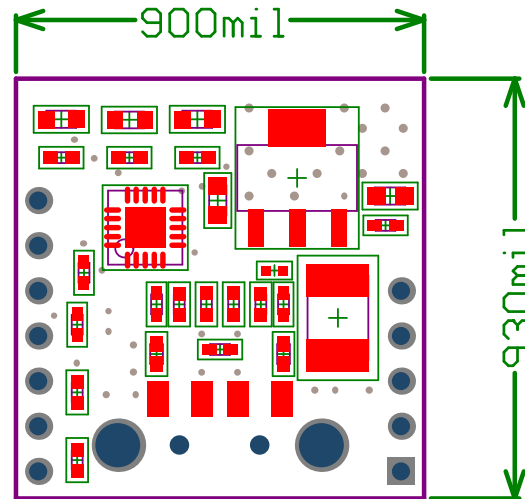


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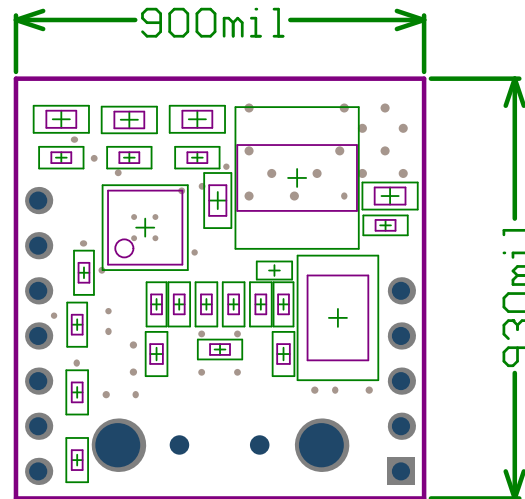


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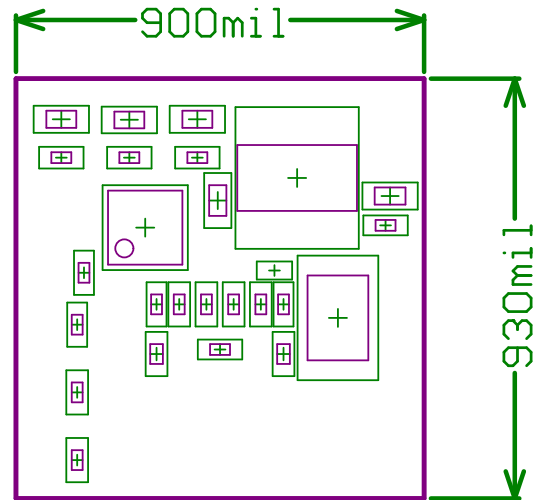


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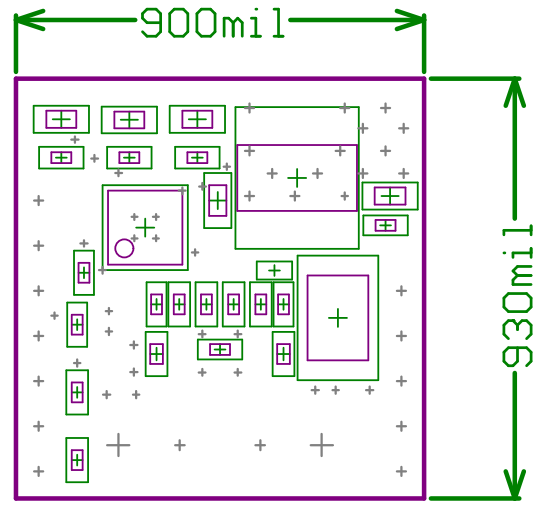


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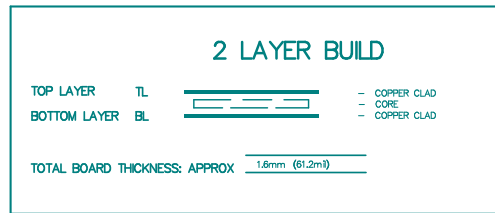
DRILL GUIDE (SLOTS)

DIMENSIONS

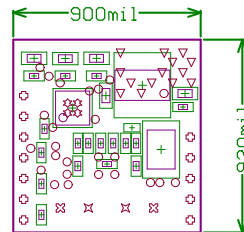
ROUTING KEEPOUT LAYER

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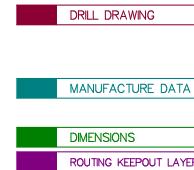
LAYER STACK CONFIGURATION



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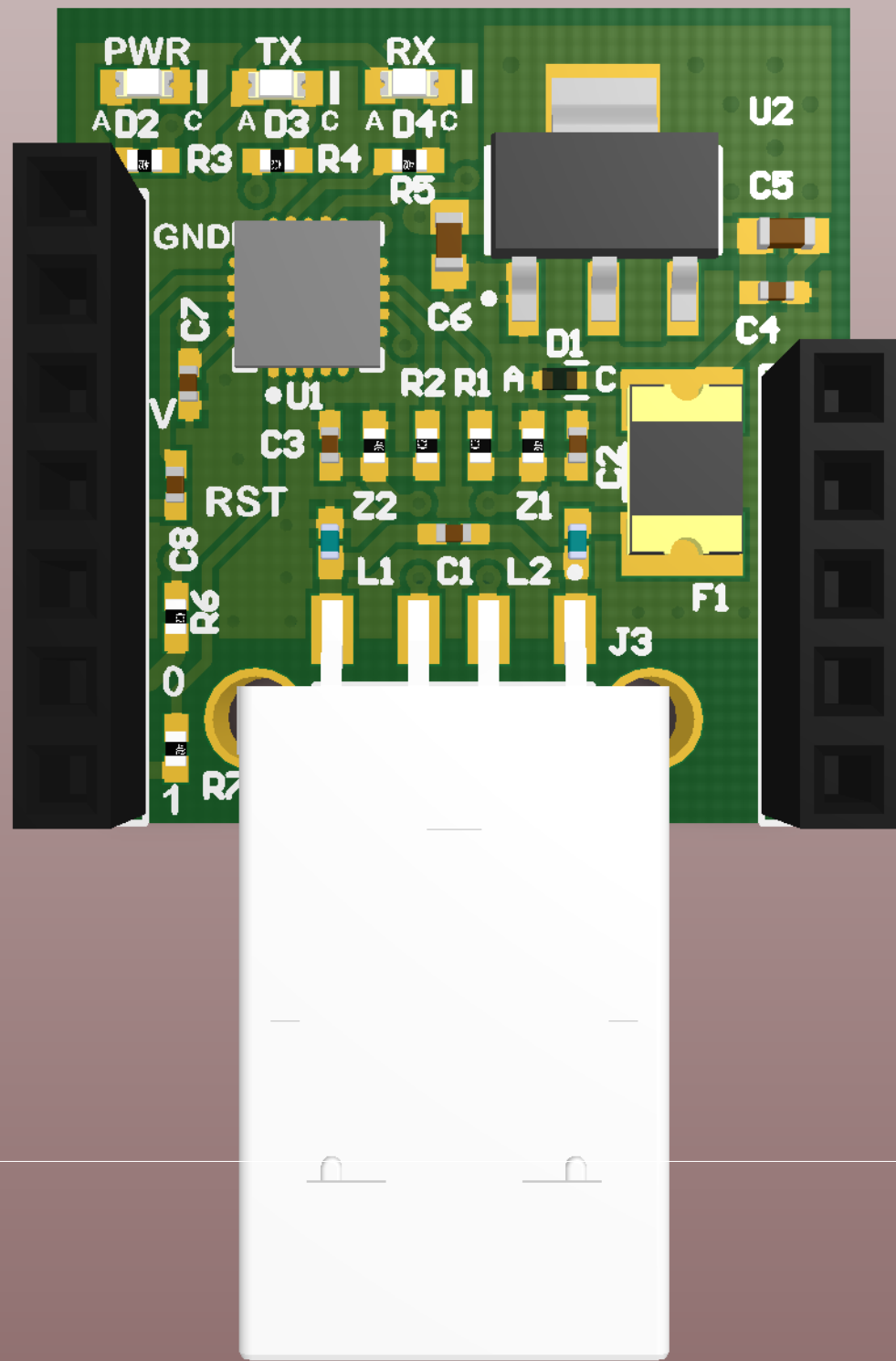


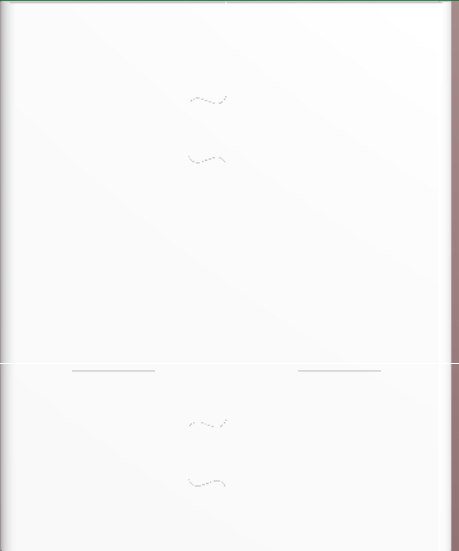
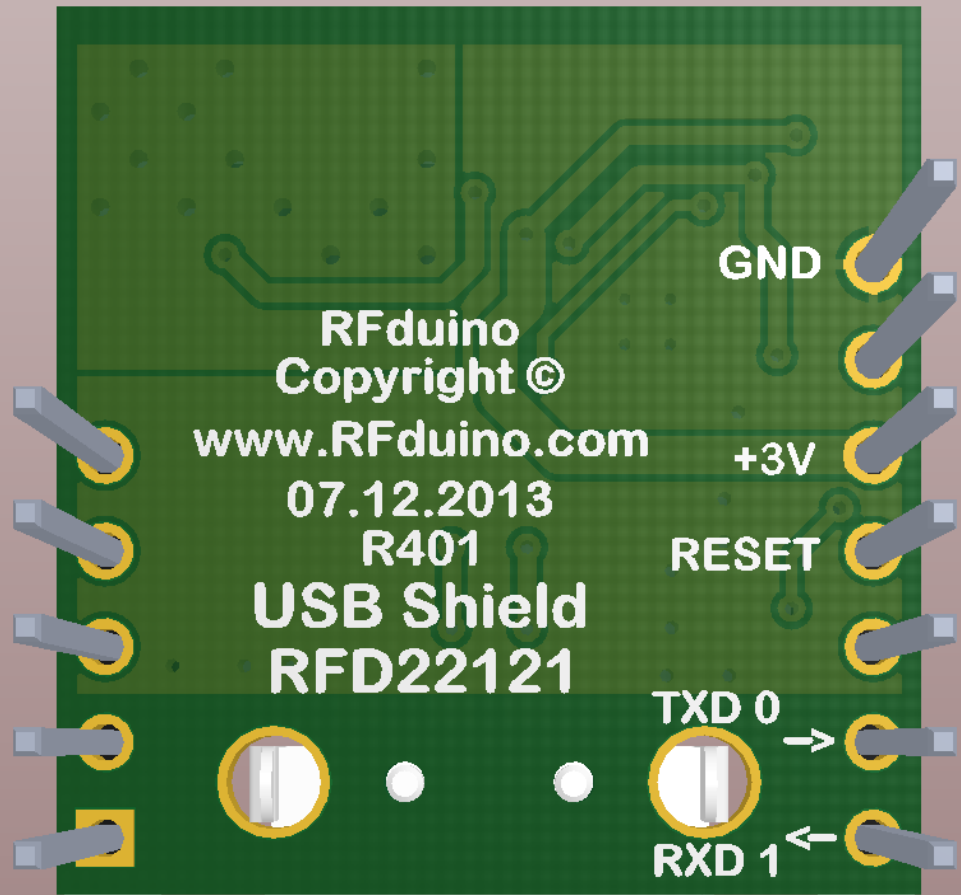
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NOTES: UNLESS OTHERWISE SPECIFIED

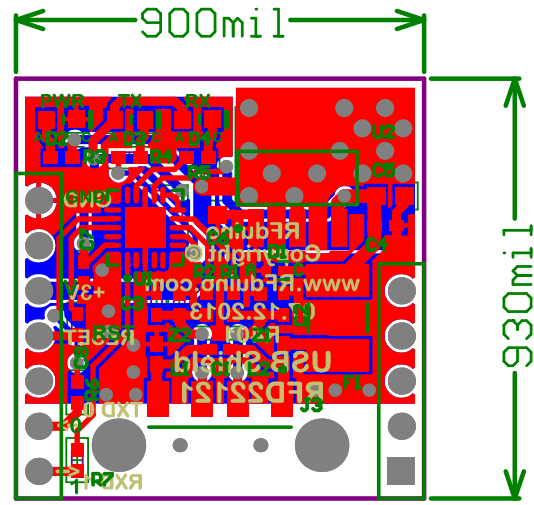
1. MATERIAL: FR4
2. FINISH: BLACK GLOSS LPI SOLDERMASK OVER BARE COPPER, BOTH SIDES OF BOARD
 ALL EXPOSED CONDUCTIVE PATTERN AREAS NOT COVERED WITH SOLDER MASK OR OTHER PLATING SHALL BE PLATED WITH IMMERSION GOLD
3. SILKSCREEN BOTH SIDES USING THE WHITE NON-CONDUCTIVE EPOXY INK,
 SILKSCREEN INC NOT ALLOWED ON COMPONENT PADS
4. NO VENDOR MARKINGS OF ANY TYPE, ON ANY LAYER OF THE PCB
 IT IS OK FOR VENDOR TO PUT MARKINGS ON THE RAILS OF THE ARRAY ONLY
5. IMPEDANCE CONTROL REQUIREMENT
6. FINISHED FAB AND WORKMANSHIP PER IPC-A-600, CLASS2
7. RoHS COMPLIANCE REQUIRED
8. ALL VIAS ARE TO BE FULLY PLUGGED WITH SOLDERMASK
9. IF THERE ARE ANY DISCREPANCIES, IT IS THE VENDORS RESPONSIBILITY TO CONTACT THE CUSTOMER
10. NO X-OUTS
11. 100% ELECTRICAL TEST FOR OPEN AND CLOSED REQUIRED

Symbol	Hit Count	Tool Size	Plated	Hole Type
☆	4	12.992mil (0.33mm)	PTH	Round
○	25	15mil (0.381mm)	PTH	Round
▽	14	20mil (0.508mm)	PTH	Round
⊕	12	40mil (1.016mm)	PTH	Round
⊗	2	43.307mil (1.1mm)	NPTH	Round
⊘	2	98.425mil (2.5mm)	PTH	Round
59 Total				





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LAYERS

TOP OVERLAY

TOP COPPER
BOTTOM COPPER

BOTTOM OVERLAY

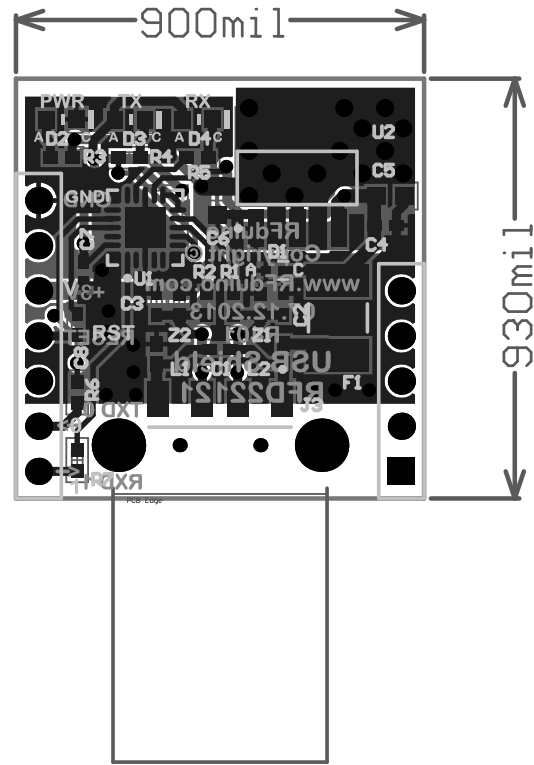
MULTILAYER

DIMENSIONS

ROUTING KEEPOUT LAYER

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LAYERS

- TOP OVERLAY
- TOP COPPER
- BOTTOM COPPER
- BOTTOM OVERLAY
- BOARD OUTLINE
- MULTILAYER
- DIMENSIONS
- ROUTING KEEPOUT LAYER

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