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Logic Test Probe

LOGIC PROBE 1



COMMON SPECIFICATIONS:

- input impedance: 1M Ω .
- width of min. pulse: 30 ns.
- max. input frequency: 17 MHz.
- measurable Logic Gate: TTL and CMOS
Logic 1 (red LED Lights) and then buzz.
2.3V \pm 0.2V 70% Vcc
Logic 0 (green LED lights) and then buzz.
0.8V \pm 0.2V 30% Vcc
- input overvoltage protection:
 \pm 220V DC/AC 15 sec.
- Pulse (yellow LED) flashing time: 500ms.
- Power supply protection: \pm 20V.

dimension	weight
212 \times 26.5 \times 18mm (main body)	65g
235 \times 78 \times 32mm (with case)	210g